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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10026241	FILING DATE 12/21/2001	CLASS 327	SUBCLASS 478	GAU 2826	EXAMINER Zweizig
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**APPLICANTS: Theobald Thomas; Leipold Ludwig;

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**CONTINUING DATA VERIFIED:
none JZ

** FOREIGN APPLICATIONS VERIFIED:
GERMANY 100 64 123.7 12/21/2000 JZ

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		WMP-IFT-007
Verified and Acknowledged Examiners's initials		
TITLE : Circuit configuration for driving a semiconductor switching element and method for same		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. 3
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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